Webinar: Capacitive Isolation Technology for Industrial Applications

Isolation, June 2018

www.ti.com/isolation

https://e2e.ti.com/support/isolation/

Agenda

- Overview of Capacitive Isolation Technology
- Capacitive Isolation structure
- Industry standards Reliability and High-Voltage Testing
- Comparison to other Isolation technologies
- Applications, End Equipment
 - PLCs
 - Motor Drives
 - Power Delivery
- TI Online resources for follow-ups
- Q&A

Introduction

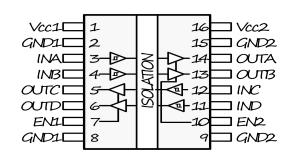
What is Isolation?

Is a means of preventing DC or uncontrolled transient current from flowing between two communicating points.

Why Isolate?

- 1. Where there is a possibility for high voltage surges that may damage equipment or harm humans.
- 2. Where interconnections involve large ground potential differences (GPDs) and disruptive ground loops are to be avoided.
- 3. Communication to high side components in motor drive systems.

Isolation enables communication between a transmitter and a receiver, referenced to very different ground potentials



Example Isolator in a 16-pin package

Isolation terminology (in brief)

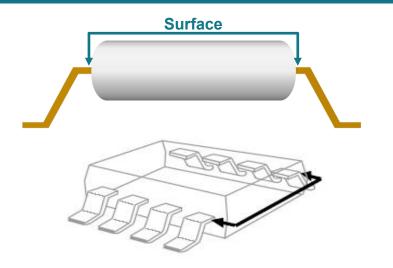
| Parameter | Definition | Relevance | |
|-------------------------|--|--|--|
| Basic Isolation | Isolation that can provide protection against high voltage as long as the barrier is intact. | Basic isolation needs to be coupled with another insulation barrier, if human access is possible. | |
| Reinforced Isolation | Isolation that is equivalent to two basic isolation barriers in series. | Reinforced isolation by itself is sufficient as a safety barrier against high voltage. | |
| V_{IOTM} | The sinusoidal voltage isolator can tolerate for 60s (defined in peak) | Tolerance to temporary overvoltage on supplies due to load changes, arcing etc. | |
| V _{ISO} | The sinusoidal voltage isolator can tolerate for 60s (defined in rms) | | |
| V_{IORM} | Maximum periodic voltage that the isolator has to handle on a continuous basis throughout its operating life (defined in peak) | The voltage that the isolator has to handle as part of normal operation (for eg. an isolated gate driver sees a pk voltage equal to the DC bus voltage). | |
| V_{IOWM} | Maximum continuous working voltage that the isolator has to on a continuous basis throughout its operating life (defined in rms) | | |
| V_{SURGE} | Maximum peak voltage of the 1.2us/50us standard surge waveform that the isolator can handle. | Represents direct and indirect lightening strikes. Min 10kV required for reinforced isolation. | |
| Creepage | Minimum distance from pins on side 1 to side 2 along the surface of the package | Limits working voltage or continuous voltage due to degradation along package surface (called tracking) | |
| Clearance | Minimum distance from pins on side 1 to side 2 through air | Limits peak voltages and surge voltages in system environment due to air breakdown | |
| CMTI | The maximum <u>rate of change</u> of ground potential difference (GND1-GND2) that the isolator can withstand without bit errors | Indicates robustness of isolator to ground noise. Very important in gate-drive applications | |

For details refer: http://www.ti.com/lit/pdf/slyy063



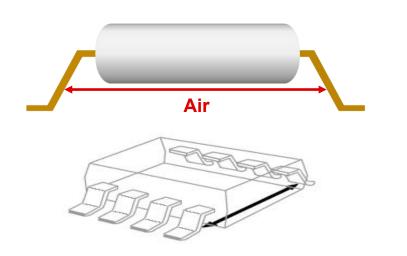
Creepage & clearance

Creepage distance



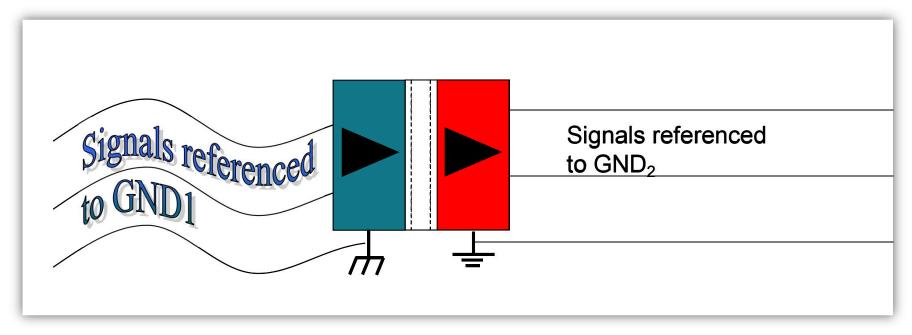
Shortest distance between two conductive leads, across isolation barrier, <u>measured</u> along surface of insulation.

Clearance distance



Shortest distance between two conductive leads, across isolation barrier, <u>measured</u> through air.

Data isolation: Common-Mode Transient Immunity



CMTI – The change in ground 1 relative ground 2. Measured in kV / µSec.

Standards that apply to Isolation

- Component level Standards:
 - IEC 60747-5-5 (VDE 0884-5-5) for Opto Isolators.
 - VDE 0884-10 / VDE 0884-11 for Capacitive/Magnetic isolators, reinforced
 - IEC 60747-17
 - UL 1577

TI Isolators must pass

System Level / End Equipment Standards

- IEC 61800-5-1, safety requirements for adjustable speed drives
- IEC 60601-1, Medical equipment standard
- IEC 61010-1, safety standard for measurement, control and Lab equipment
- IEC 60950-1, Telecom equipment standard

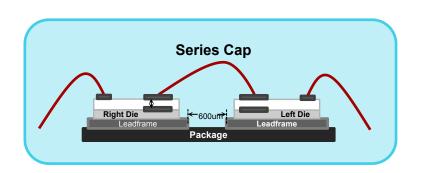
EMC and Emissions

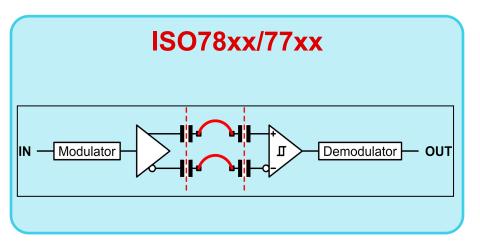
- IEC 61000-4-x, ESD, EFT, Surge, RF immunity
- CISPR22 or equivalent, EM emissions

Customer boards and systems must pass – TI Isolators should enable



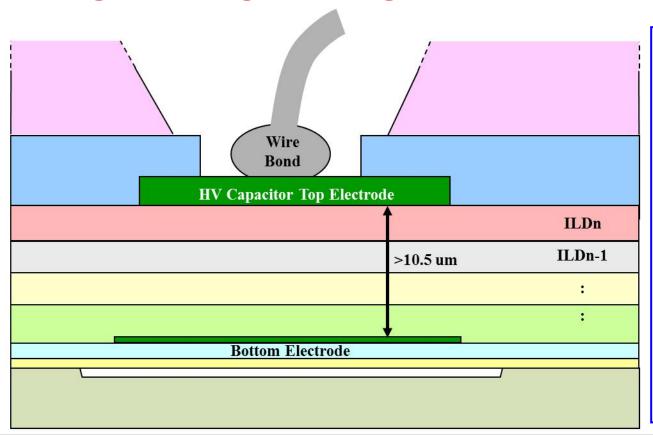
Reinforced Isolation Technology





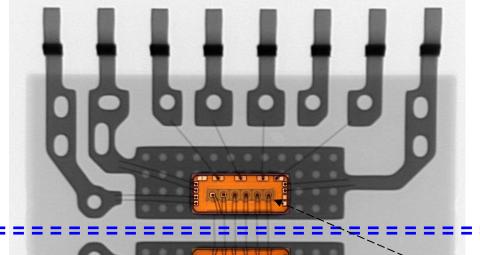
- Reinforced Isolation realized by Series Capacitor configuration
- Combined Isolation capacitor thickness is ~25um (SiO₂)
- 12.8kV surge voltage, 8kVpeak transient over-voltage, 1.5kVrms working voltage

Design for High Voltage

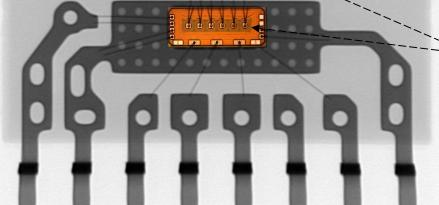


- Reinforced Isolation barrier <u>consists of two</u> high voltage capacitors
 - One on each die
 - Connected in series
- <u>Each capacitor</u> has a thick SiO₂ dielectric
 - Multiple standard dielectric layers
 - CVD films
 - CMP planarization
- ➤ Very high isolation voltage capability

Design for High Voltage



X-Ray of 16DW SOIC package



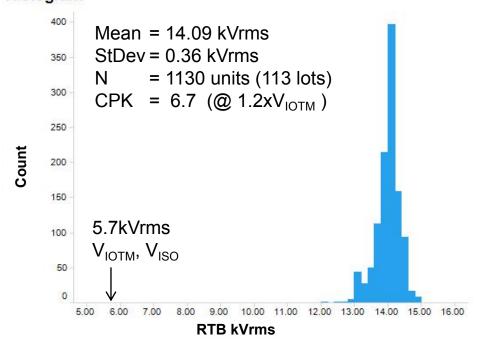
Reinforced Isolation Barrier:

- Wide Body Package
 - External clearance and creepage>8mm for 16DW SOIC (shown)
 - Internal spacing between die pads inside the package,600um
- Two die, each with high voltage isolation capacitors
 - HV capacitors on each die
 - Connected in series



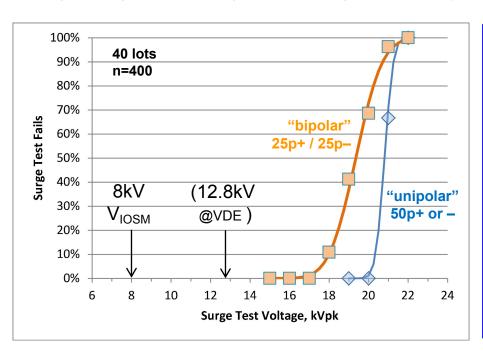
- Ramp to Breakdown (RTB): Qualification Test
 - Voltage ramped at a rate of 1 kV_{rms}/second until a breakdown occurs in the isolation barrier

Histogram



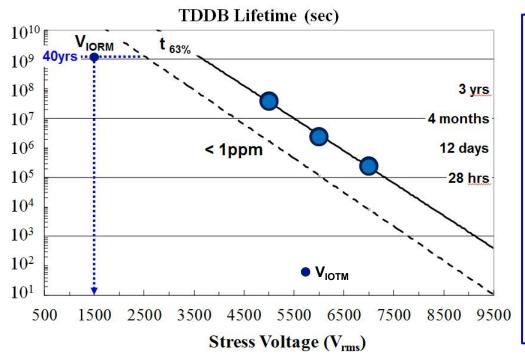
- Ramp-to-Breakdown sample test.
- <u>Verifies high margin</u> of the actual breakdown voltage above the maximum transient isolation voltage (V_{IOTM})
 - For example: ISO77xx, AMC1303 $V_{IOTM} = 5.7 \text{ kVrms}$
- ➤ Very high isolation voltage capability

- Surge Test: Qualification Test
 - Surge verifies the DUT's immunity to *very* high voltage levels over a short period of time (such as a lightening strike).
 Surge peak voltage required by external standards (IEC 61000-4-5 & VDE 0884-1x):



- Surge characterization sample test.
- Verifies high margin of the actual breakdown voltage above the maximum surge isolation voltage (V_{IOSM})
 - For example: ISO77xx, AMC1303 $V_{IOSM} = 8.0 \text{ kVpk}$
- ➤ Very high isolation voltage capability

- The main Isolation electrical lifetime test is TDDB: Qualification Test
 - Standard methodology for determining the lifetime of a dielectric as a function of Voltage



- TDDB is accelerated lifetime testing of the isolation barrier
 - Weibull statistics at each voltage
 - Multiple voltages. Fit to model:Time-to-Fail = A * exp (-γ * E)
- TDDB verifies the lifetime
 - For example: ISO77xx, AMC1303 V_{IORM} , $V_{IOWM} = 1.5 \text{ kVrms}$
- ➤ First technology in the industry to certify to VDE 0884-11

Method B1 & Method A High-Voltage Testing:

- The Method B1 and A tests consist of two parts
 - An Isolation test: HV test to screen out units with defective caps
 - An Partial Discharge test: We follow the same 5pCoulomb test standard that is required for all isolation technologies
- Method A: Qualification Test
 - Isolation stress test at <u>VIOTM</u> for <u>60s</u>; Partial discharge test at 1.6xVIORM for <u>10s</u>
 - Method A is required by external standards IEC/VDE to verify V_{IOTM} on a sample basis – Quarterly monitor
- Method B1: Qualification Test & Production Test
 - Isolation stress test at 1.2x VIOTM for 1s; Partial discharge test at 1.875x VIORM for 1s
 - External standards IEC/VDE requires that all reinforced isolation units must be tested in production

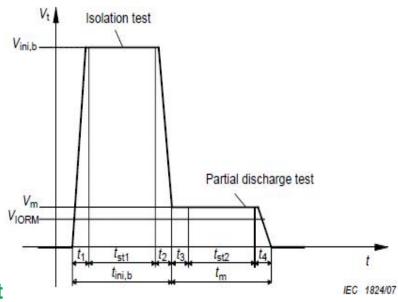


Figure: Method-B1 & Method A testing profiles

SiO₂ Insulation

TI Capacitive Isolation uses SiO₂ as the Isolation Dielectric.

- SiO₂ advantages compared to competing HV isolative material are: reliability and dielectric strength

| Insulator Materials | Dielectric Strength, 1 sec | Dielectric Strength, 40yr reliability |
|------------------------------|----------------------------|---------------------------------------|
| Air | ~1 Vrms/um | |
| Epoxies | ~20 Vrms/um | |
| Silica filled Mold compounds | ~100 Vrms/um | |
| Polyimide | ~300 Vrms/um | ~20 Vrms/um |
| SiO_2 | ~500 Vrms/um | ~100 Vrms/um |

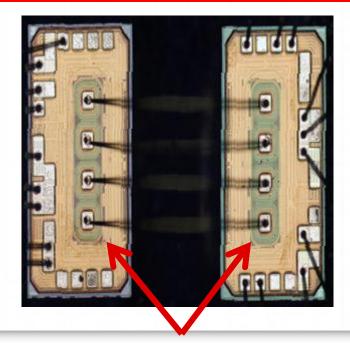
Table 1: Commonly used materials for High-Voltage isolation (SiO₂ has the highest dielectric strength and best reliability)

<u>Dielectric Thickness (SiO₂) Fabrication Process Control & Monitor:</u>

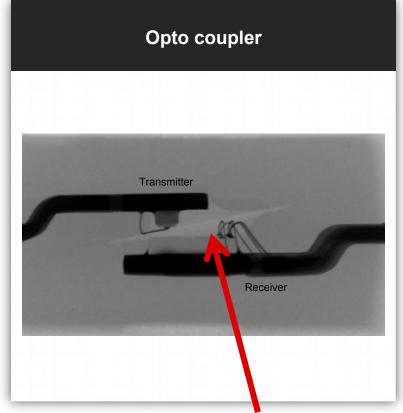
- Repeatability and Reproducibility SiO₂ thickness monitored using two methods:
 - Oxide thickness sample measured in the wafer fab for each of the layers of the dielectric stack
 - Tox by capacitance measurement on test structures on every production wafer after fabrication
- Thickness is tracked on all wafers for each fabrication lot as required by UL standard

Comparison to other Isolation technologies

TI capacitive coupler

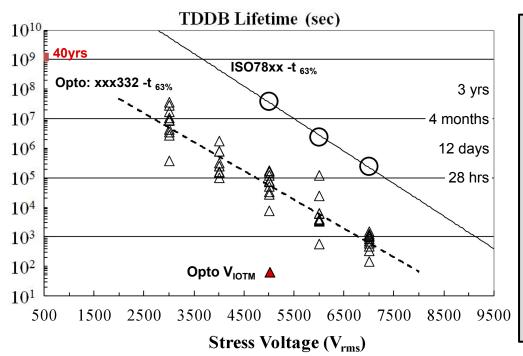


Isolation built with 2 capacitors (TI couplers with reinforced isolation)



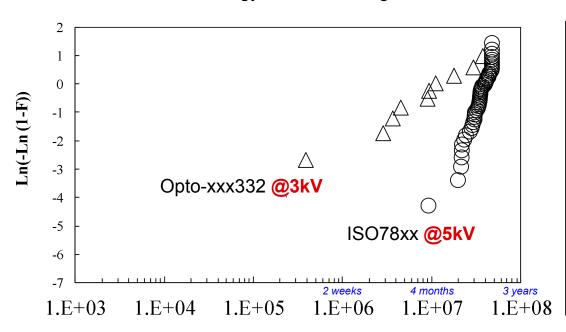
Isolation built with transparent silicone and thin tape

- The main <u>Isolation</u> electrical lifetime test is <u>TDDB</u>: <u>Qualification Test</u>
 - Standard methodology for determining the lifetime of a dielectric as a function of Voltage



- Like all dielectric materials, optocoupler does degrade over time under high voltage stress
- TDDB testing is needed for all HV isolation technologies, in addition to partial discharge testing on each unit
- TDDB data of opto-coupler fits standard TDDB E-Field model, even down to 3kVrms testing

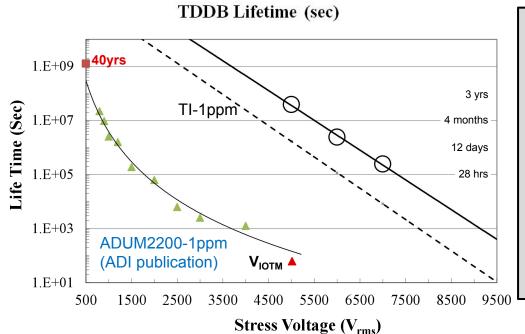
- The main <u>Isolation</u> electrical lifetime test is <u>TDDB</u>: <u>Qualification Test</u>
 - Standard methodology for determining the lifetime of a dielectric as a function of Voltage



- ISO78xx shows much tighter distribution at 5kVrms than opto at 3kVrms TDDB
- At 5kVrms, ISO78xx shows much higher MTTF (meantime-to-fail), 402 days than 103 days of Opto-xxx332 at 3kV

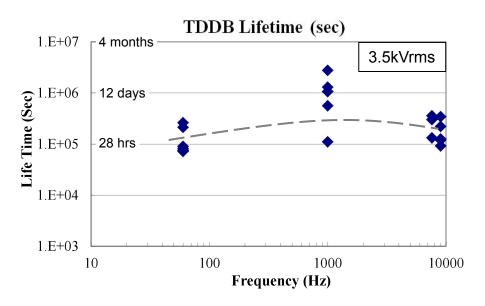
Time (sec)

- The main Isolation electrical lifetime test is TDDB: Qualification Test
 - Standard methodology for determining the lifetime of a dielectric as a function of Voltage

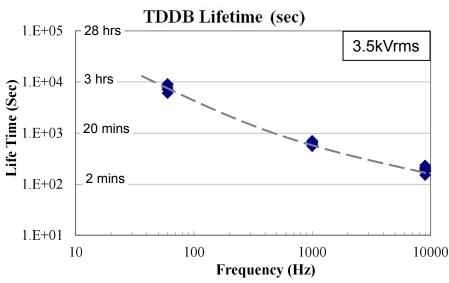


- Polyimide dielectric follows a different exponential relation to the stress voltage ("power-law").
- Polyimide lifetime degrades after exposure to ambient moisture.
- Polymer TDDB must be tested after units have reached equilibrium with ambient moisture.

- The main Isolation electrical lifetime test is TDDB: Qualification Test
 - Standard methodology for determining the lifetime of a dielectric as a function of Voltage



12.7um SiO₂ single capacitor



25um Polyimide capacitor Lifetime degrades at higher frequency

TI Isolation Technology

Reliability – longer product lifetimes

Industry's highest reliability, reinforced insulation barrier

- Built on Tl's 180nm CMOS Process Technology with Series Capacitor Architecture
- Longest lifetime reliability; >1.5 kV_{RMS} working voltage over 100+ years
- Superior transient protection for harsh environments, >12.8kV surge

Performance & Integration – better for industrial and automotive

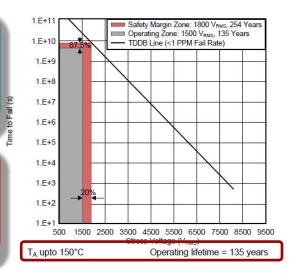
TI portfolio integration of Basic & Reinforced Isolation offers leading solutions

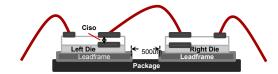
- Lowest Emissions, Highest CMTI, and Power Efficiency
- · Meets or exceeds industry certifications standards for insulation, emissions, and immunity
- Isolation plus high performance Analog, Data Conversion, Interface and Power

Ease of Design – Partnered support with FAE's & Customers

Complete Portfolio of Discrete and Integrated Isolation Solutions, Tools and Support

- Over 175 TI Reference Designs, White Papers, App Notes and EVMs Available
- Isolation dedicated E2E Online Support Forum & Industry standards and certification experts







Bullet-Proof Electro Magnetic Compatibility

- EMC is equivalent to Robustness and critical to the Industrial and Automotive Markets
- TI's Reinforced Capacitive Isolation sets new levels for EMC & CMTI
 - Exceeds WW EMC requirements
 - 100kV/us CMTI enables next-gen driver technology GaN/SiC FETs

| EMC metric | Governing Standard | Exceeds Level |
|---------------------------------|---|--|
| Radiated Immunity | IEC-61000-4-3 | Class A > 20V/m @ < 1GHz. > 16V/m @ > 1GHz |
| Conducted Immunity | IEC-61000-4-6 | Class A >15Vrms @ < 80MHz |
| Power frequency, Pulse magnetic | IEC-61000-4-8,9 | Class 5 >4kA/m |
| ESD | IEC-61000-4-2 | Level 4 8kV Contact |
| EFT/Burst | IEC-61000-4-4 | Supply/IO 4kV/2kV |
| Surge | IEC-61000-4-5 | Class X - > 10kV |
| Emissions | EN55022/CISPR22B / SAE J1752-3 / FCC15 | Class B At least 4dB better than competition |
| СМТІ | IEC-60747-17 | > 100kV/us min |







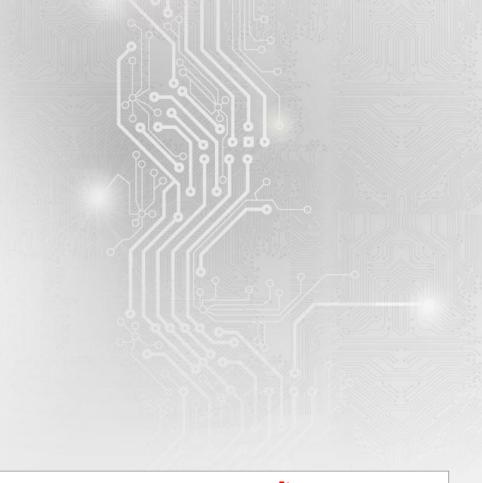




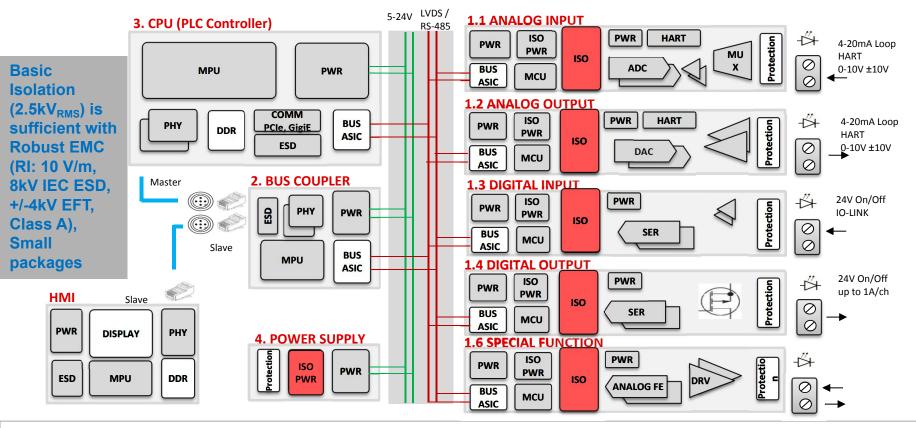
Key Comments

- TI's isolation family of products has <u>high voltage capability that exceeds the requirements for reinforced isolation</u>.
- The <u>quality of HV isolation</u> is demonstrated by <u>substantial margins</u> using statistical test methods.
- The <u>reliability of the HV isolation process technology is proven</u> by TDDB, which is the industry standard method of proving lifetime at use conditions.
- For more details, see the TI white paper "Enabling high voltage signal isolation quality and reliability."

PLCs



PLC System Block Diagram - Architecture

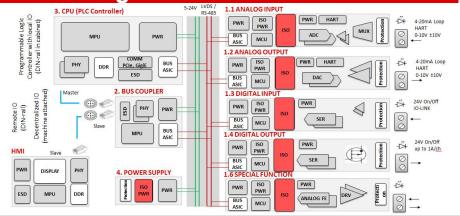


PLC

Key Specifications

- 500Vrms Working Voltage, 2.5kVrms (1s, VISO), basic isolation
- ESD (6kV-8kV) and EFT (4kV)
 - · Especially on ISO-RS485 and ISOCAN
- Low power consumption (to relieve thermal constraints in compact designs)
- EMC Immunity and Emissions
- · Small packages and multi-channel configurations
- Low propagation delays (for SPI timing closure)
- · Lower controller side power supply

Block Diagram



Key Parts

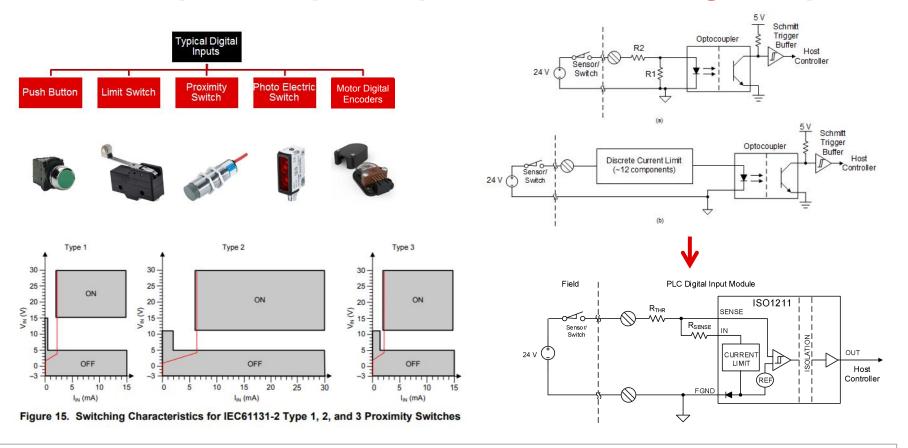
- ISO121x
 - Does not need field side supply
 - · Fully integrated solution for digital input modules
- SN650x
 - Flexible output voltages, high efficiency (80%+), low cost, small footprint, flexible output voltages, ultra low emissions, SSC, Soft Start
- ISOWATT
 - Compact design, fully integrated power+signal isolation. Low emissions, High Efficiency, Soft start
- ISO77xx
 - 2.25V min power supply, > 8kV IEC ESD, EMC hardened, low prop delays, flexible package options (16DW, 8D, 16 DBQ), 6channel options
- ISOCAN & ISO-RS485
 - · Integrated solutions for communication module

Key Collateral

- How to design 48V, 110V, and 240V DC and AC Digital Inputs
- How To Simplify Isolated 24-V PLC Digital Input Module Designs
- Isolation for PLC blog series (coming soon)
- 96-channel ISO1212 TI Design (coming soon)



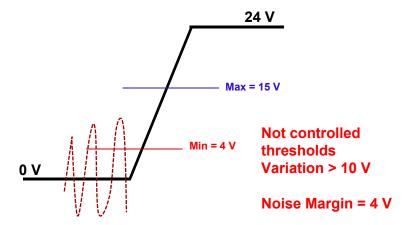
ISO121x Replaces Optocouplers in Isolated Digital Inputs



Better Noise Margins – Low Thresholds and

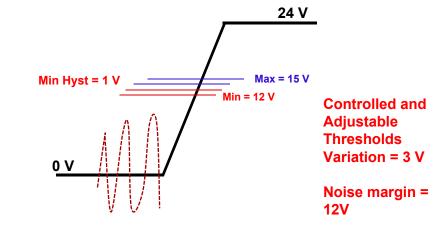
Hysteresis Opto-Coupler

Usually opto-coupler based inputs have very high variation in thresholds, or very high current at 30V input



ISO121x

| PARAMETER | | TEST CONDITIONS | MIN | TYP | MAX | UNIT | |
|---|--|--|------|-------|-------|------|--|
| VOLTAG | E TRANSITION THRESHOLD ON FIELD SID | DE | | | - 10 | | |
| | Y | $R_{SENSE} = 562 \Omega$, $R_{THR} = 0 \Omega$, see Figure 11 | 6.5 | 7 | | v | |
| | Low level threshold voltage at module input (including R _{THR}) for output high | $R_{SENSE} = 562 \Omega$, $R_{THR} = 1 k\Omega$, see Figure 11 | 8.7 | 9.2 | 98 | | |
| | input (motioning it (pgt) for output ingin | $R_{SENSE} = 562 \Omega$, $R_{THR} = 4 k\Omega$, see Figure 11 | 15.2 | 15.8 | - 8 | | |
| V _{IH} High level threshold voltage at module input (including R _{THR}) for output low | nazione nos vondo sem los test | $R_{SENSE} = 562 \Omega$, $R_{THR} = 0 \Omega$, see Figure 11 | | 8.2 | 8.55 | v | |
| | | $R_{SENSE} = 582 \Omega$, $R_{THR} = 1 k\Omega$, see Figure 11 | | 10.4 | 10.95 | | |
| | $R_{SENSE} = 582 \Omega$, $R_{THR} = 4 k\Omega$, see Figure 11 | | 17 | 18.25 | | | |
| | | $R_{SENSE} = 582 \Omega$, $R_{THR} = 0 \Omega$, see Figure 11 | 1 | 1.2 | | < | |
| | Threshold voltage hysteresis at module input | $R_{SENSE} = 562 \Omega$, $R_{THR} = 1 k\Omega$, see Figure 11 | 1 | 1.2 | | | |
| | | R _{SENSE} = 562 Ω, R _{THR} = 4 kΩ, see Figure 11 | 1 | 1.2 | - 6 | | |



Thermals: ISO1212 vs Traditional Solutions

ISO121x Devices Reduce Board Temperatures vs Traditional Solutions

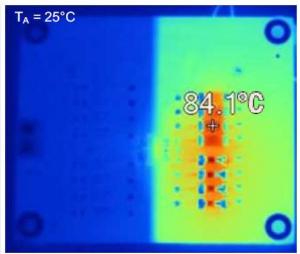
ISO1212:

0.72W power dissipation for 8-ch at 36V input

Needs 0.25W series resistors.



a) 8-Ch With ISO1212

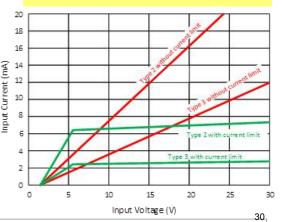


b) 8-Ch Traditional Solution Without Current Limit

ISO121x operates ~40 C cooler vs. traditional solutions

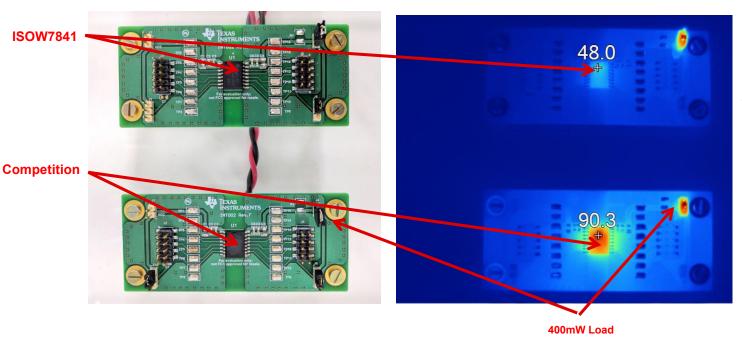
Traditional 3.6W power dissipation for 8-channels at 36V input

Needs 0.5W-1W series resistors



ISOW's efficiency makes microPLC possible

Image showing setup
Infra-Red image showing hotspots



With PLCs getting smaller and more dense, thermal performance is critical.

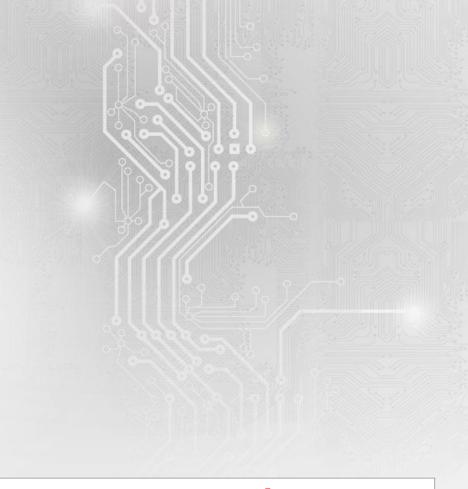
TI is ~40°C cooler than the competition at full load.

TI Isolation Products for PLC

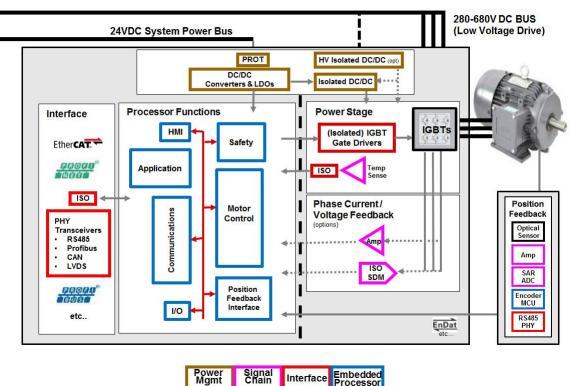
| Products | Description | Key Benefits |
|---|---|--|
| ISO121x | 24V isolated digital input receiver with 2mA current limit | Does not need field side supply, fully integrated solution for digital input modules (1 & 2 channel configurations) |
| ISO77xx | 100 Mbps reinforced digital isolators | 2.25V min power supply, > 8kV IEC ESD, EMC hardened, low prop delays, flexible package options (16DW, 8D, 16 DBQ), 6-channel options |
| SN6501 and SN6505 | Push-pull transformer drivers for upto 5W isolated power supplies | Flexible output voltages, high efficiency (80%+), low cost, small footprint, flexible output voltages, ultra low emissions, SSC, Soft Start |
| Isolated RS-485 and Isolated CAN portfolios | Isolated RS-485 and Isolated CAN portfolios | Flexible portfolio of interconnect solutions for communication module |
| ISOW78xx | Digital isolators and transceivers with integrated power supply | Compact design, fully integrated power + signal isolation, low emissions, high efficiency, soft start |



Motor Drives



Complete Motor Drive System



Different modules in this EE:

- Power stage
- Control module
- Position feedback
- 4. Main power supply
- 5. HMI, I/O, Keyboard
- 6. Wired, Wireless communication

Signal Isolation is present in:

- a. PWM driving (control stage
- to Power stage): RS485, CMOS
- b. Voltage, current, Temp sensing
- c. Communication interface(data log,

Maintenance, PLC control)

e. CAN,RS485, Ethernet, Digital I/O

Power isolation present in:

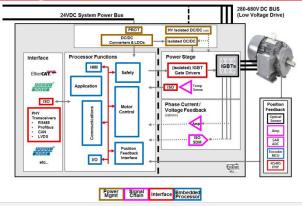
- a. IGBT gate drive power supply
- AC/DC main power supply

Motor Drives

Key Specifications

- High Working Voltage Motor Drives Mains > 690Vrms
- Noise Immunity for robust isolation barrier against IEC EFT
- Small size save space & optimize layout. Especially important in Servo drives
- CMTI Needed for gate driving where a large voltage switching happens.
- Robust isolation barrier (functional safety)

Block Diagram



Key Parts

- ISO77xx
 - ISO7760 for gate drive in Servo Control, PWM drive
- ISO121x
 - · Safety Inputs, Safety torque off
 - Fast switching ideal for motor positioning
- ISOWATT
 - Compact design, fully integrated power+signal isolation. Low emissions, High Efficiency, Soft start
- ISO35/3082/3086/1176
 - RS485 Communication Port
- ISO1042
 - CAN Communication Port
- SN650x
 - IGBT Power Supply
 - Flexible output voltages, high efficiency (80%+), low cost, small footprint, flexible output voltages, ultra low emissions, SSC, Soft Start

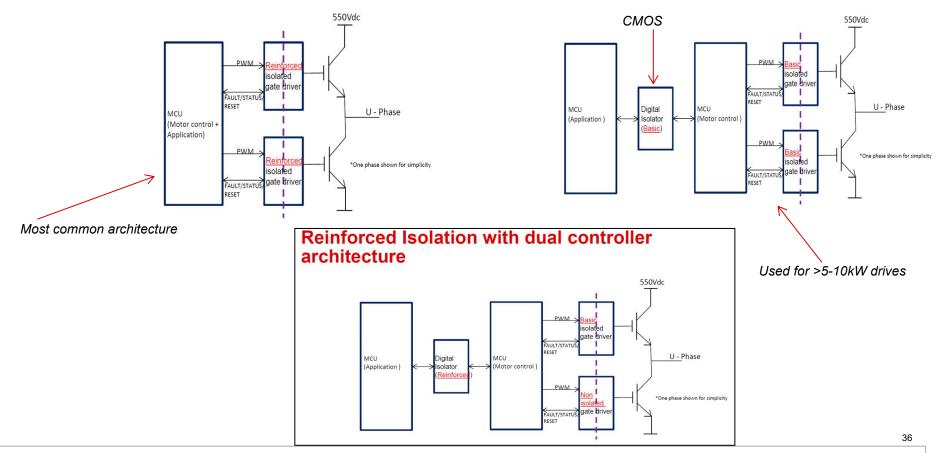
Key Collateral

- Isolation in AC Motor Drives: Understanding the IEC 61800-5-1 Safety Standard
- 3-phase IPM Based Inverter with Single Package Reinforced Isolator Reference Design
- How to Improve Speed and Reliability of Isolated Digital Inputs in Motor Drives
- ISO7760DW Advantages in IPM based Motor Drives (coming soon)



Reinforced Isolation with single Controller

Basic + Basic Isolation with dual controller architecture



Motor Drives Key Specs

- •<u>High working voltage</u>: Tl' Isolation barrier is at least 25% better than nearest competition. This is required because input mains supply for Drives can go to 690Vrms or even higher.
 - ✓ ISO77xx (1kVrms / 1500Vpk working voltage)
 - ✓ ISO78xx (1500Vrms DW / 2000Vrms DWW).
 - ✓ Only isolator in industry with DWW package (>14mm creepage/clearance).
 - ✓ ISO1042 (CAN) has same isolation technology as ISO77xx, high working voltage on isolated transceivers
- •Noise immunity: Due to fast switching/Ldi/dt, isolation barrier for gate drive/communications needs robust isolation barrier against IEC EFT.
- •Small size: Multiple isolation channels in same package saves space against optos
 - ✓ ISO77xx available till **6 channels** in various package options
- •<u>CMTI</u>: highest in industry. Needed for gate driving at large voltage switching happens. Future SiC/GaN FETs may require even higher CMTI
 - ✓ ISO78xx Minimum CMTI Spec: +/-100 kV/µs [Industry Leading]

High Channel Density

Digital Isolation Packages: ISO77xx Family





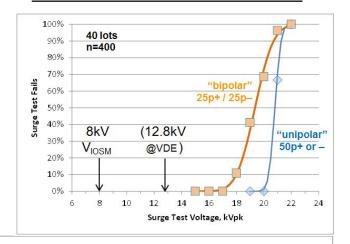




Images drawn to scale

| Package | Identifier | Length (mm) | Width (mm) | Creepage (mm) | Channels |
|-------------|------------|-------------|------------|---------------|---------------|
| 8-pin SOIC | D | 4.90 | 3.91 | 4 | 1, 2 |
| 16-pin SSOP | DBQ | 4.90 | 3.90 | 3.7 | 3, 4, 6 |
| 8-pin SOIC | DWV | 5.85 | 7.50 | 8 | 2 |
| 16-pin SOIC | DW | 10.30 | 7.50 | 8 | 1, 2, 3, 4, 6 |

Robust isolation barrier





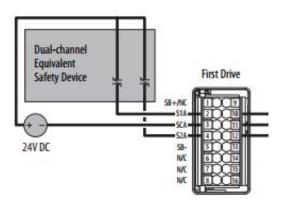
ISO121x in Motor Drives: STO

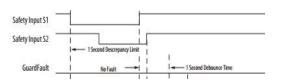
STO (Safe Torque Off):

- Safe Torque off is a safety function implemented in most drives. This is used as an emergency stop for the motor. This is usually a 24-V input.
- These signals are high by default and implemented with opto-couplers.
 - Default High is used since this automatically provides wire break wire break will assert STO and stop the motor.
- ISO1212 or ISO1211 provide a better alternative for STO.



- Key benefits of ISO1212 vs. Optocouplers for STO:
 - Precise voltage thresholds and hysteresis for noise immunity.
 - Lower FIT (Failure in Time): ISO121x has 10-20 times lower FIT than optocouplers.
 - No Aging: Since STO signals are always ON, optocouplers have a greater risk of aging.

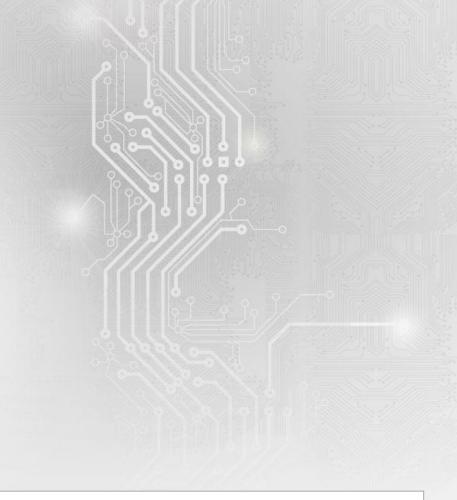




TI Isolation Products for Motor Drives

| Products | Description | Key Benefits |
|-------------------------|---|---|
| ISO121x | 24V isolated digital input receiver with 2mA current limit | Does not need field side supply, fully integrated solution for digital input modules, safety inputs, safety torque off, IEC61131-2 Type 1, 2, and 3 |
| ISO77xx | 100 Mbps reinforced digital isolators | 2.25V min power supply, > 8kV IEC ESD, EMC hardened, low prop delays, flexible package options (16DW, 8D, 16 DBQ), 6-channel options |
| ISOW78xx | Digital isolators and transceivers with integrated power supply | Compact design, fully integrated power + signal isolation, low emissions, high efficiency, soft start |
| ISO1042 | Isolated CAN transceiver | Improved HV ISO performance, CMTI, transceiver performance |
| SN6501 and SN6505 | Push-pull transformer drivers for up to 5W isolated power supplies | Flexible output voltages, high efficiency (80%+), low cost, small footprint, flexible output voltages, ultra low emissions, SSC, soft start |
| ISO5852s and ISO5452 | +2.5A/-5A, isolated, high CMTI, single channel driver with integrated protection | Component-level reinforced rating, improved system performance, enabling low power & efficient solutions, high immunity for noisy environments, high reliability in harsh environments |
| AMC1311 | 2 Vin, reinforced isolated amplifier for voltage sensing with industry's highest CMTI up to 140 μV/s | Reinforced isolation rating with input optimized for voltage sensing, 2 grades of performance: AMC1311 and AMC1311B, highest accuracy measurements with low offset and gain error & longer lifetime and reliability with extended temperature range of -55 to 125C. |
| AMC1300 | ± 250 mVin reinforced isolated amplifier for current sensing with industry's highest CMTI up to 140 μ V/s | Reinforced isolation rating with input optimized for current sensing, 2 grades of performance: AMC1300 and AMC1300B, highest accuracy measurements with low offset and gain error and longer lifetime and reliability with extended temperature range of -55 to 125C. |

Power Delivery



Single Phase Online UPS

Rectifier + Inverter +Charger Inverter Stage VDC+ 400V to 800VDC Isolated Gate driver Gate driver Gate driver Gate driver DC Bus DC Bus Current Voltage Contactor Contactor Isolated Isolated Isolated Coil Gate driver Gate driver Gate driver x2V Solenoid VDC-Solenoid Temp Voltages Voltages Sensina Currents Currents Temp Quad FAULTs from IGBT **Gate Drivers** Differential Sensing Line Driver **Battery Charger &** PWM to IGBT Quad **Boost Converter** VDC+ **Gate Drivers Digital Signal** Differential REF Isolated Controller / Line Driver Gate driver Micro Controller RTC **Battery Current** CAN Single controller for TEMP Relav PFC Rectifier, Charger Battery Voltage RS-485 Drivers and Inverter Isolation ESD Isolated Battery USB Gate driver **Fan Drive** Stack MCU Load Wireless **Switches** DDR Connectivity **Switching Current**



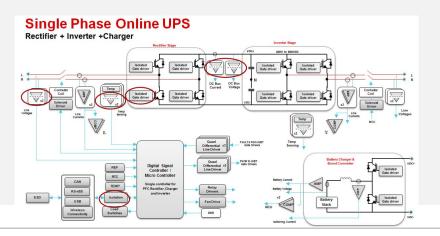


Power Delivery

Key Specifications

- Small package size as network power supplies are shrinking
- · Multiple channels in one package
- Cost
- Isolation (basic / reinforced depending on where it is used)
- Power consumption (for efficiency specs on PSU)

Block Diagram



Key Parts

- ISO77xx: Meets basic / reinforced isolation. Package and channel options allow it to fit most sockets.
- SN650x: Size and efficiency make it suitable for IGBT Power Supply
- ISO35/3082/3086: Isolated RS485 for communication in module
- ISO1042: Isolated CAN Transceiver for communication in module
- ISOW784x (For RS485 / CAN Interface + isolated power)
- ISO154x (Telecom rectifier / Network PSU) communication between MCU1 and MCU2

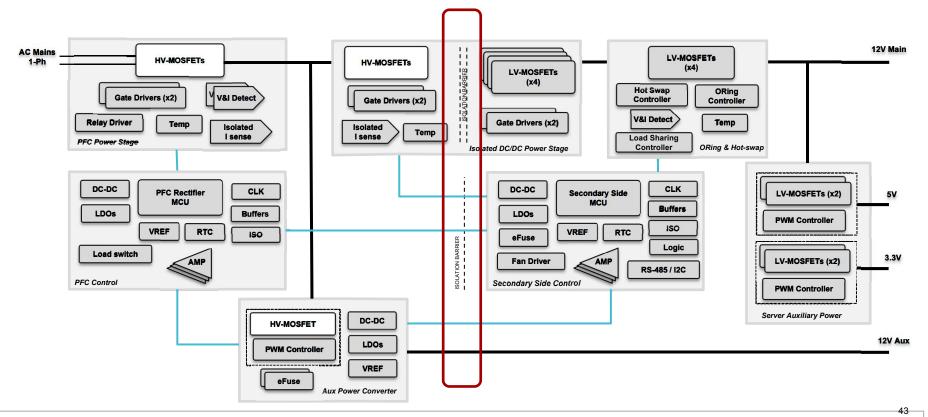
Key Collateral

- 92% Efficiency, CoC Tier2, 65W USB-PD AC/DC Adapter Reference Design in High Power Density – ISO7710
- <u>High-Efficiency</u>, 1.6-kW High-Density GaN-Based 1-MHz CrM Totem-Pole PFC Converter Reference Design

Network and Server Power Supply

Distributed Control Architecture





Power Delivery Requirements

- Small package size as network power supplies are shrinking
 - -ISO77xx available in DW and DBQ packages
- Multiple channels in one package
 - -ISO77xx available in up to 6 channel variants
- Isolation (basic / reinforced depending on where it is used)
- Power consumption (for efficiency specs on PSU)
 - -SN650x up to 80% Efficient
- CAN for communication between parallel modules in UPS
 - -ISO1042 offers isolation and CAN transceiver all in one device

TI Isolation Products for Power Delivery

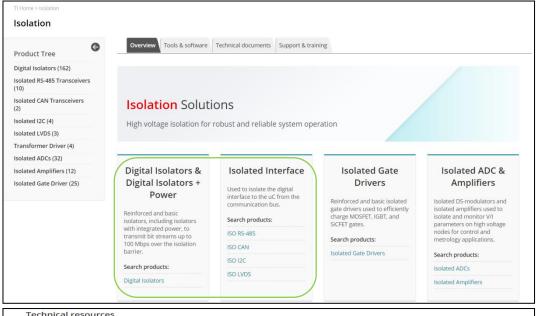
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| SN6501 and SN6505 | Push-pull transformer drivers for up to 5W isolated power supplies | High efficiency (80%+), flexible output voltages, low cost, small footprint, flexible output voltages, ultra low emissions, SSC, soft start | |
| ISO35/3082/3086 | Isolated RS-485 transceivers | Isolated full-duplex and half-duplex transceivers with high ESD protection, 50kV/us CMTI, 20Mbps | |
| ISO1042 | Isolated CAN transceiver | Improved HV ISO performance, CMTI, transceiver performance | |
| ISOW78xx | Digital isolators and transceivers with integrated power supply | Compact design, fully integrated power + signal isolation, low emissions, high efficiency, soft start | |
| ISO154x | Isolated I2C | Bidirectional I2C isolator, supports up to 1-MHz | |
| UCC21520 and UCC21222 | Isolated dual-channel gate driver with 4-A source and 6-A sink peak current with best-in-class propagation delay and pulse-width distortion | Drop-in replacement with better performance, higher drive could eliminate buffer stages, flexible settings to prevent shoot-through in ½ bridge applications, provides high noise immunity for fast/high current designs such as power supplies | |
| AMC1311 | 2 Vin, reinforced isolated amplifier for voltage sensing with industry's highest CMTI up to 140 μV/s | Reinforced isolation rating with input optimized for voltage sensing, 2 grades of performance: AMC1311 and AMC1311B, highest accuracy measurements with low offset and gain error & longer lifetime and reliability with extended temperature range of -55 to 125C. | |
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TI Online Resources



www.ti.com/isolation



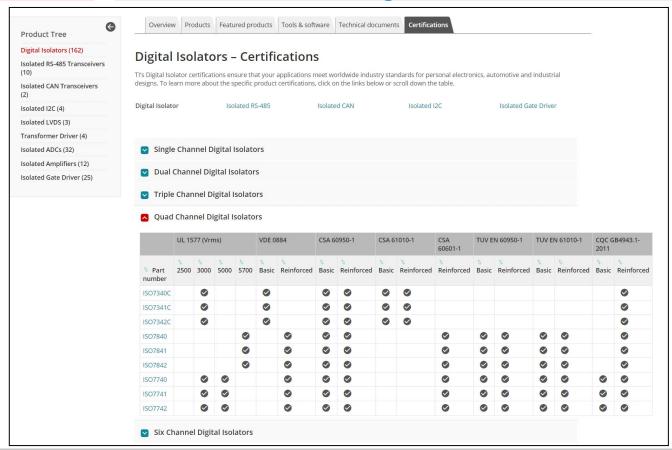
- More learning
- Actual Designs



- Follow-up material
- Online support!



Certifications: www.ti.com/isolation/digital-isolators/certifications.html



Q&A

Support forum for follow-up:

https://e2e.ti.com/support/isolation/



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